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Robin E. Barnum (Depositor's name)  
*[Signature]* (Signature)  
March 22, 2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/943,603	08/30/2001	Gary L. Swoboda	TI-30491	2424

TITLE OF INVENTION: USING SIGN EXTENSION TO COMPRESS ON-CHIP DATA PROCESSOR TRACE AND TIMING INFORMATION FOR EXPORT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	06/12/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
GEBRESILASSIE, KIBROM K	2128	703-024000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.  
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2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,  
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- 1 Robert D. Marshall, Jr.  
2 W. James Brady, III  
3 Frederick J. Telecky, Jr.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Texas Instruments Incorporated

Dallas, TX USA

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature Robert D. Marshall, Jr.Date March 22, 2006Typed or printed name Robert D. Marshall, Jr.Registration No. 28,527

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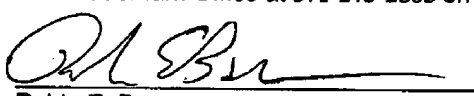
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of **TI-30491**  
Gary L. Swoboda Art Unit: 2128  
Serial No.: 09/943,603 Examiner: Kibrom K. Gebresilassie  
Filed: August 30, 2001 Conf. No.: 2424  
For: Using Sign Extension to Compress On-Chip Data Processor Trace and Timing Information  
for Export

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Robin E. Barnum

Date March 22, 2006

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<b>NAME OF INVENTOR(S):</b> Gary L. Swoboda		<b>RECEIPT DATE &amp; SERIAL NO.:</b>	
<b>TITLE OF INVENTION:</b> Using Sign Extension to Compress On-Chip Data Processor Trace and Timing Information for Export		<b>Serial No.: 09/943,603</b>	
<b>TI FILE NO.:</b> <b>TI-30491</b>		<b>Filing Date:</b> August 30, 2001	
<b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>			
<b>FAXED:</b> 3/22/06 <b>DUE:</b> 6/12/06 <b>ATTY/SECY:</b> RDM/rcb			

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